

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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TECHNOLOGY CENTER 2800

In re Application of

Nakagawa, K.

Serial No.: 09/688,203

Filed: October 16, 2000



Group Art Unit: 2823

Examiner: Brewster, W.

For: METHOD OF MANUFACTURING SEMICONDUCTOR MEMORY DEVICE

Honorable Assistant Commissioner of Patents

Washington, D.C. 20231

Box AF

**RESPONSE UNDER 37 C.F.R. §1.116**

Sir:

Prior to imminent appeal and in response to the Office Action dated December 4, 2002, for the above-reference Application, please reconsider the rejection of record in view of the following.

**REMARKS**

Claims 1-6 and 13 are all of the claims pending in the present Application. Claims 1-4 stand rejected under 35 USC §102(e) as being anticipated by US Patent No. 6,265,739 to Yaegashi et al. Claims 5 and 6 stand rejected under 35 USC §103(a) as unpatentable over Yaegashi, further in view of US Patent No. 5,824,584 to Chen et al. The rejection of record seems to ignore claim 13, but it is assumed for purpose of this response that this claim is also rejected.

These rejections are respectfully traversed in view of the following discussion.